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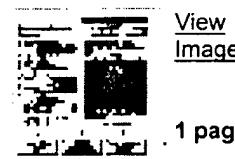
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>Title: **JP2000113606A2: METHOD AND DEVICE FOR CORRECTING READ DATA
ERROR FROM DATA RECORD MEDIUM**

Country: **JP Japan**

Kind: **A2 Document Laid open to Public inspection!**



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Assignee: **INTERNATL BUSINESS MACH CORP <IBM>
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Published / Filed: **2000-04-21 / 1998-10-05**

Application Number: **JP1998000282233**

IPC Code: **G11B 20/18;**

Priority Number: **1998-10-05 JP1998000282233**

Abstract:

PROBLEM TO BE SOLVED: To revise a bit shift error irrespective of ECC by a method wherein a read signal from a medium is converted into a bit string, and a gray bit having possibilities in an error in the bit string to generate gray bit information, and an error bit is corrected from the information, and the corrected bit string is decoded.

SOLUTION: A read channel 11 converts a read signal from a medium 10 into a read data 13 comprising a bit string, and a gray bit 14 having possibilities in an error in a bit string is detected by a gray bit detection circuit 12, and is fed to a decoder 16 via a mark detector 15. The decoder 16 inspects data from the gray bit 14 in a RLL(run length limited) error correction circuit 17, and corrects a discovered data error during transfer to a conversion table 18, and the corrected data are decoded by the conversion table 18. At this time, an error correction is made by an error correction circuit 19 using a conversion table.

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INPADOC Legal Status: **None** Get Now: [Family Legal Status Report](#)

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Forward References:	PDF	Patent	Pub.Date	Inventor	Assignee	Title
	<input checked="" type="checkbox"/>	US6587294	2003-07-01	Ushio; Teruhiko	International Business Machines Corporation	Method and apparatus for error correction of stored data